

AMENDMENT TO THE CLAIMS

This listing of claims will replace all prior versions of claims in the application.

Listing of Claims:

1. (currently amended) A circuit comprising:

a first transistor having a first terminal, a second terminal and a first gate terminal, wherein the first terminal is coupled to a power supply;

a resistor-capacitor (RC) filter coupled between the power supply and ground and coupled to the first gate terminal to reduce noise from the power supply at the first gate terminal;

a second transistor having a third terminal, a fourth terminal and a second gate terminal, wherein the third terminal is coupled to the second terminal to have the first transistor supply current to the second transistor, and wherein the fourth terminal is an output terminal to provide an output voltage; and

an operational amplifier circuit having its amplifier output coupled to the second gate terminal, wherein the fourth terminal is coupled to a feedback network of the operational amplifier circuit to regulate the output voltage.

2-7. (canceled)

8. (previously presented) The circuit as recited in claim 1 wherein an input of the operational amplifier circuit is coupled to an output of a bandgap generator, which sets a reference to regulate the output voltage.

9-18 (canceled)

19. (currently amended) A carrier medium comprising a database which is operated upon by a program executable on a computer system, the program operating on the database to perform a portion of a process to fabricate an integrated circuit including circuitry described by the database, the circuitry described in the database including:

a first transistor having a first terminal, a second terminal and a first gate terminal, wherein the first terminal is coupled to a power supply;

a resistor-capacitor (RC) filter coupled between the power supply and ground and coupled to the first gate terminal to reduce noise from the power supply at the first gate terminal;

a second transistor having a third terminal, a fourth terminal and a second gate terminal, wherein the third terminal is coupled to the second terminal to have the first transistor supply current to the second transistor, and wherein the fourth terminal is an output terminal to provide an output voltage; and

an operational amplifier circuit having its amplifier output coupled to the second gate terminal, wherein the fourth terminal is coupled to a feedback network of the operational amplifier circuit to regulate the output voltage.

20-25. (canceled)

26. (previously presented) The carrier medium as recited in claim 19 wherein an input of the operational amplifier circuit is coupled to an output of a bandgap generator, which sets a reference to regulate the output voltage.

27-29. (canceled)